
Safenet Sentinel Dongle Crack Software -I [PATCHED]

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1. Field of the Invention This invention relates to an apparatus and method for controlling the output of semiconductor power devices, and more particularly to a power output stage or power integrated circuit device, which includes a pair of shunt-connected transistors for controlling the respective current and voltage of each high-side and low-side devices associated therewith, where the devices have a common drain connection.

2. Description of the Related Art High-side and low-side devices are required for controlling certain functions in integrated circuits having high power dissipation devices, e.g., LDMOS devices, insulated gate bipolar transistor (IGBT) devices, etc. Prior art devices are disclosed in the following patents: U.S. Pat. No. 5,923,850, entitled, DOUBLE WIDE SHUNT MOSFET WITH SURGICALLY IMPROVED HIGH-SIDE AND LOW-SIDE CONTROLS, describes a power device having a common connection for biasing and controlling the high-side and low-side transistors which, however, are used to control the high-side and low-side devices, not the high-side and low-side transistors. U.S. Pat. No. 6,308,833, entitled, TRIPLE WIDE SHUNT MOSFET WITH SURGICALLY IMPROVED HIGH-SIDE CONTROLS, describes a device having common drain connections for the high-side and low-side transistors. Common source connections for the high-side and low-side devices are described in U.S. Pat. No. 6,175,519, entitled, DOUBLE

WIDE SHUNT MOSFET WITH SURGICALLY IMPROVED HIGH-SIDE AND LOW-SIDE CONTROLS. U.S. Pat. No. 5,
